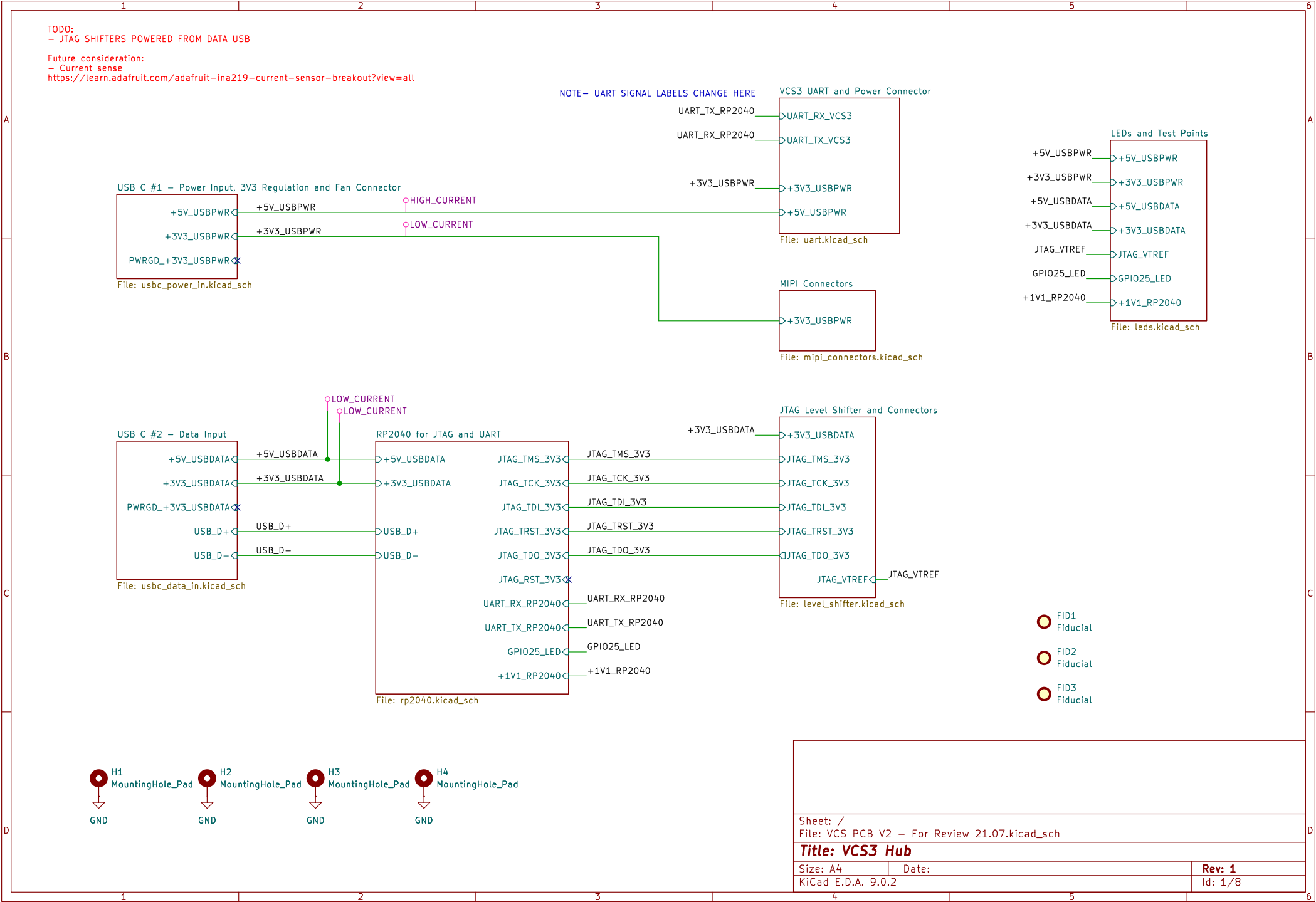
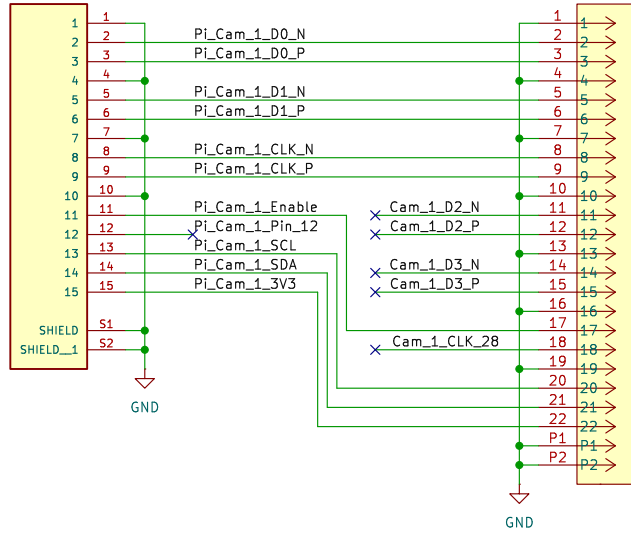




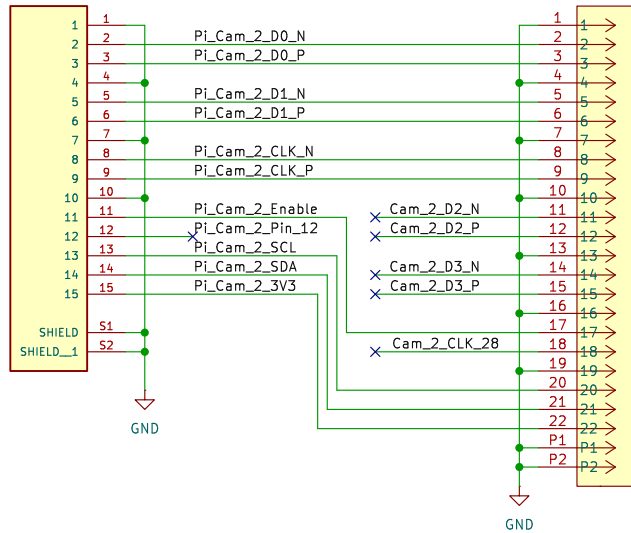
J1
SFW15R-1STE1LF

15 to 22 pin MIPI #1



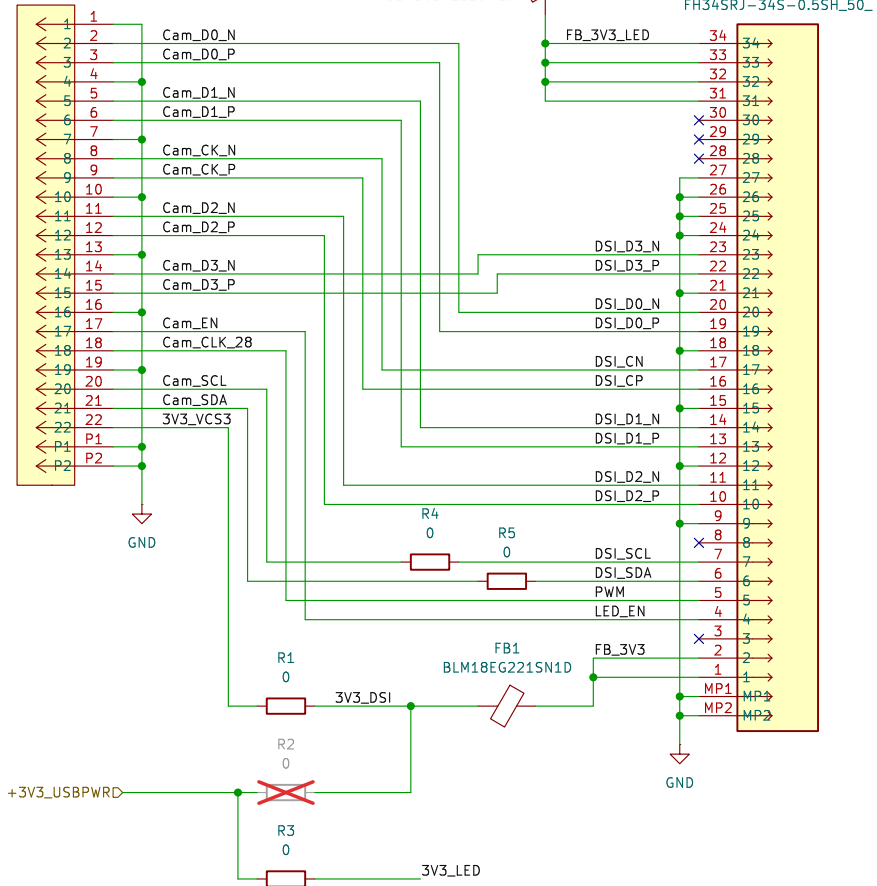
J2
SFW15R-1STE1LF

15 to 22 pin MIPI #2



J5
5034802200

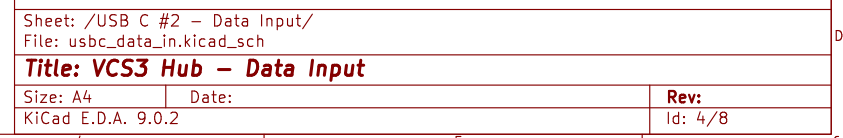
22 to 34 pin MIPI

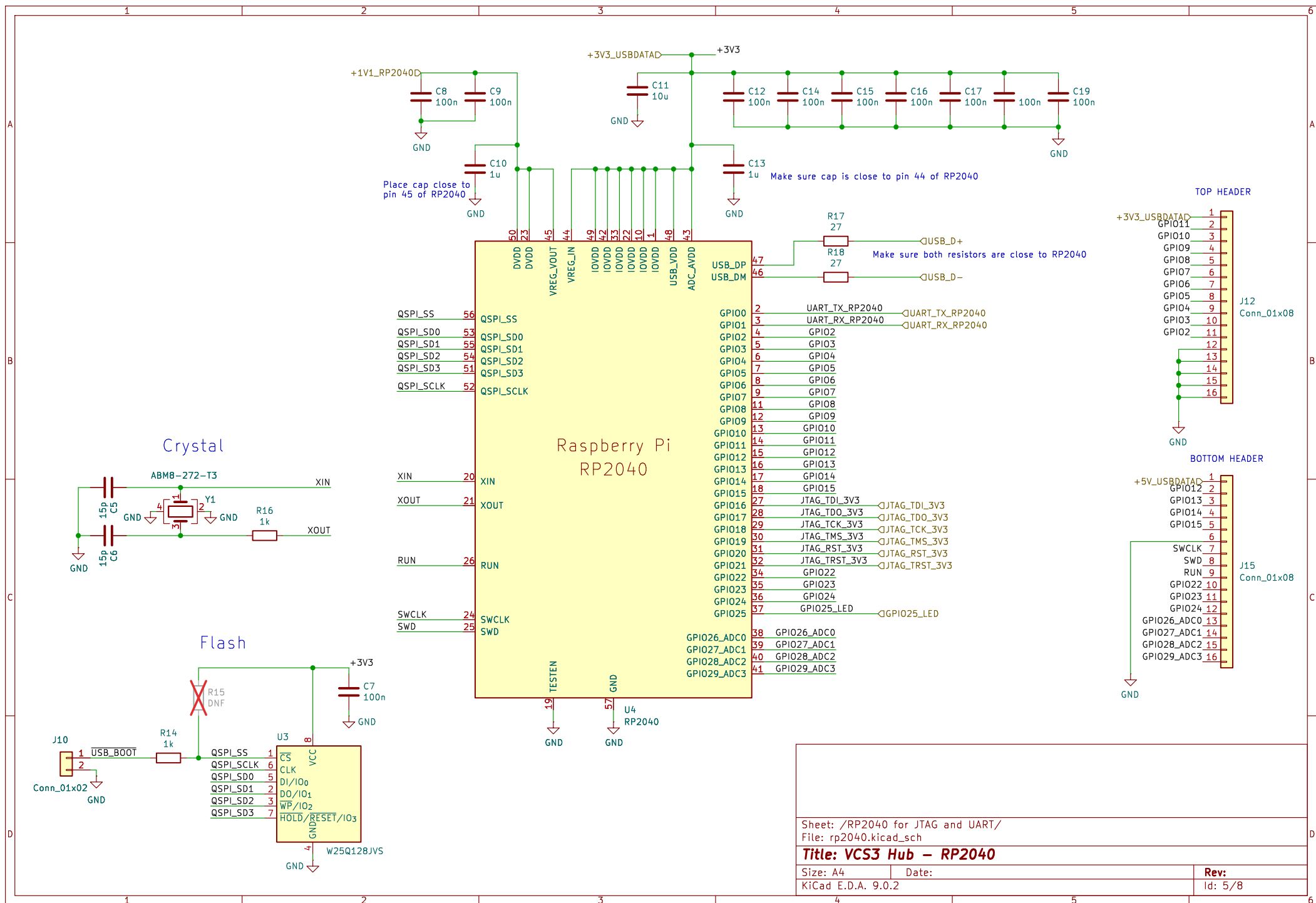


J3
5034802200

J4
5034802200







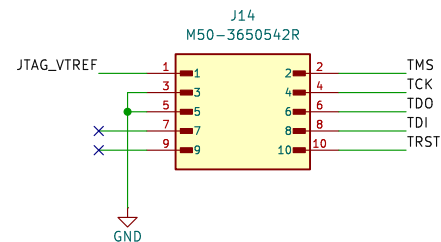
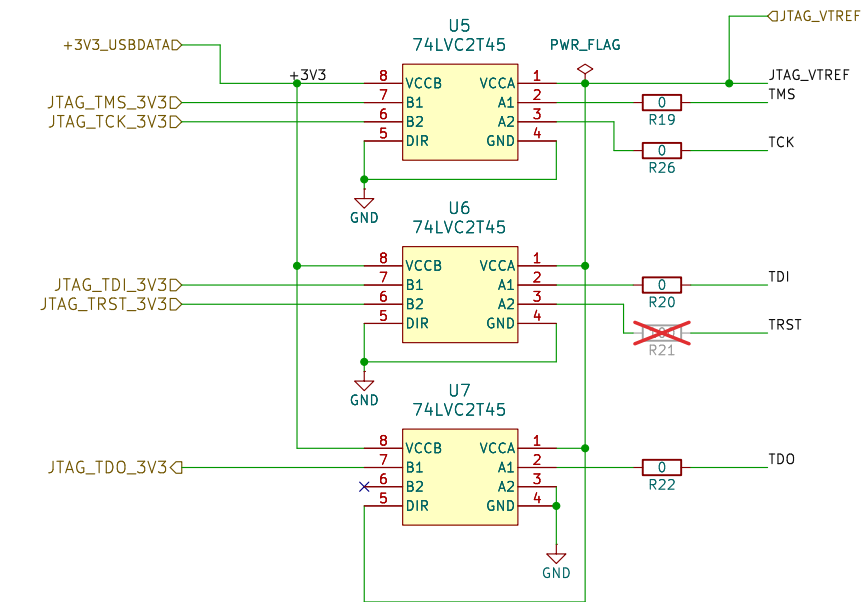
1 2 3 4 5 6

A

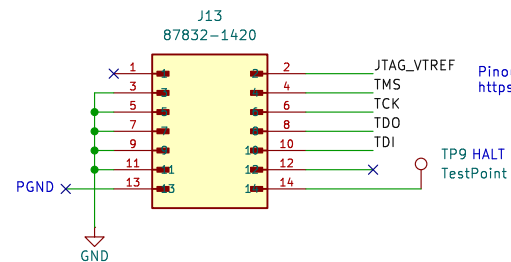
B

C

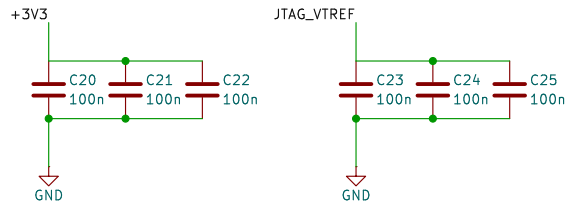
D



Pinout:
<https://riverloopsecurity.com/blog/2021/05/hw-101-jtag/>



Pinout:
<https://docs.amd.com/v/u/en-US/ds593>



Sheet: /JTAG Level Shifter and Connectors/
File: level_shifter.kicad_sch

Title: VCS3 Hub - Level Shifter

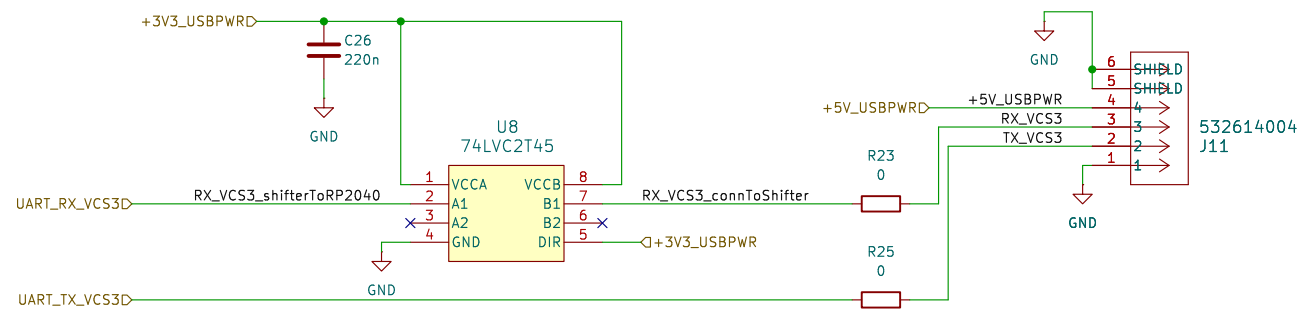
Size: A4
KiCad E.D.A. 9.0.2

Date:

Rev:
Id: 6/8

1 2 3 4 5 6

This level shifter is used as a buffer.
It prevents the "RX VCS3" signal from powering the VCS3 if there is no USB PWR connected.



Sheet: /VCS3 UART and Power Connector/
File: uart.kicad_sch

Title: VCS3 Hub – VCS3 UART and Power Connector

Size: A4

Date:

Rev:

Size: A4
KiCad E.D.A. 9.0.2

Id: 7/8

